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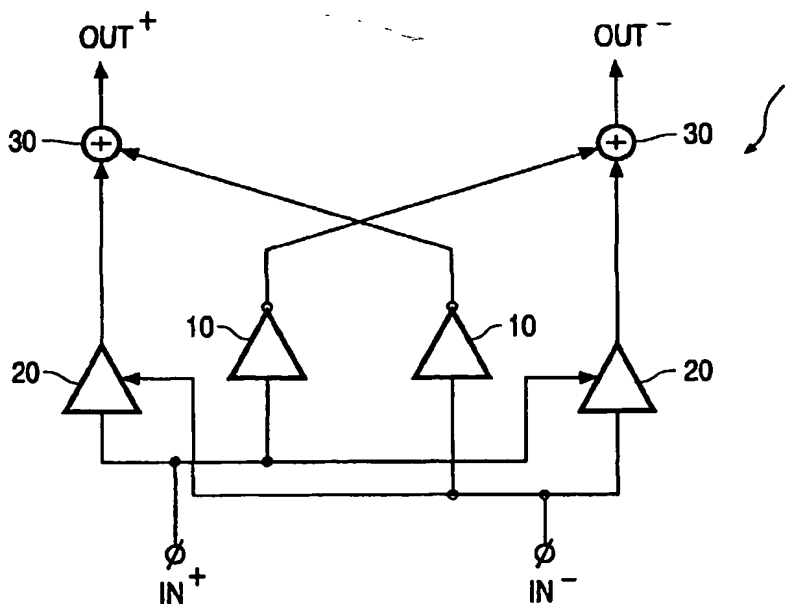
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(54) Title: A DIFFERENTIAL AMPLIFIER



(57) Abstract: A differential amplifier for amplifying an input differential signal having two components (In+, In-) substantially in anti-phase to each other and generating an output differential signal having two differential components (Out+, Out-). The amplifier comprises a pair of inverters coupled to a pair of adders receiving the input differential signal. The amplifier is characterized in that it further comprises a pair of controllable buffers for receiving the input differential signal and outputting a signal to the pair of adders. A bias of the said pair of buffers is cross-controlled by the input differential signal for controlling an amplification of said pair of controllable buffers.